

ROBUST COMPRESSION OF ^{10/13} TEST RESPONSES ⁽¹⁵⁾

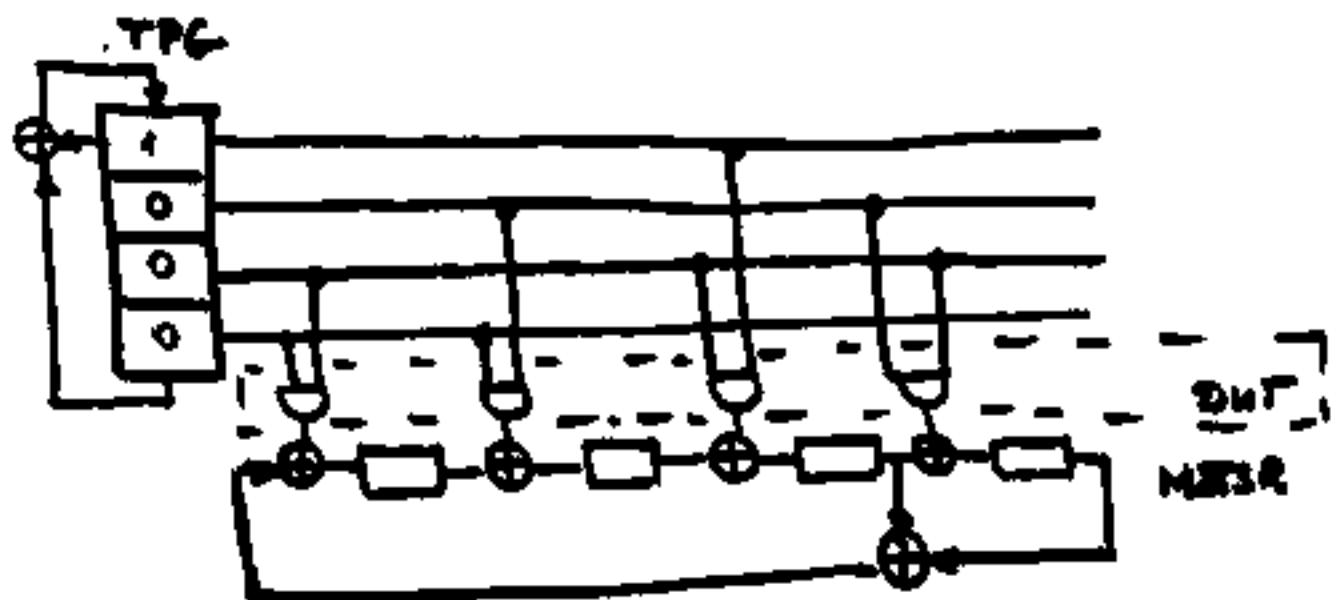
GOAL: CONSTRUCT A COMPRESSOR SUCH THAT ALIASING PROB. does not depend on a distribution of errors

SOLUTION: QUADRATIC COMPRESSORS

FOR ANY ERROR PATTERN probability of masking this error pattern is 2^{-r} where r is a number of bits in the quadratic signature.

Linear compressors (SIGNATURE ANALYZERS, MISRs, LFSRs) are not robust: their performances (aliasing) depend on a device-under-test (DUT) and a selected test

EXAMPLE



- $m=4$. INITIAL STATE OF TEST PATTERN GENERATOR (TPG) = 1000
- TEST IS EXHAUSTIVE (EXCEPT FOR 0000)
- ALL (!) SINGLE and all multiple stuck-at faults are detected by but MASKED ~~at~~ IN THE MISR.

LINER COMPRESSORS ARE NOT ROBUST!

BUILT
FROM
FFS + XORS

H - data compression matrix

S - signature

$$S = HZ$$

$$\hat{S} = H\hat{Z}$$

$$\hat{S} = \hat{S} \quad \text{- ALIASING}$$

$$HZ = H\hat{Z}$$

$$H(Z \oplus \hat{Z}) = 0$$

$$He = 0$$

kernel H

Prob of detection $\in \{0, 1\}$ FOR A GIVEN error.

Let z is a fault-free response (n-bits) (2)

$\tilde{z} = z \oplus e$ faulty response

e - error (n-bits)

$S = f(\tilde{z}) = f(z \oplus e)$ - SIGNATURE

ALIASING OF e

$$f(z) = f(z \oplus e)$$

Prob of masking e :

$$P(e) = 2^{-r} \mid z: f(z) = f(z \oplus e)$$

The compressor is robust iff

$$P(e) = 2^{-r} \text{ FOR EVERY } e$$

r is THE SIZE (number of bits in the signature)

IF THE COMPRESSOR IS
LINEAR (BUILT BY XOR GATES
AND FFs, E.G. LFSRs)

$$f(z \oplus e) = f(z) \oplus f(e)$$

AND

$$f(z) = f(z \oplus e) \Leftrightarrow f(e) = 0$$

THUS, FOR LINEAR COMPRESSORS

$P(e)$ IS EITHER 0 OR 1,

AND ALL LINEAR COMPRESSORS

ARE NOT ROBUST

FOR ANY LFSR IT IS POSSIBLE TO

CONSTRUCT A DUT SUCH THAT EVEN

FOR EXHAUSTIVE TESTING OF THIS DUT

AND COMPRESSING TEST RESPONSES BY
THE LFSR WILL RESULT IN A LOW FAULT
COVERAGE FOR SSES.

EXAMPLE $r=3$, QUADRATIC COMPRESSURE 14

Let output stream for a DUT

is 101 011 110 010 101 0110

TEST RESPONSE.

1. Select a primitive polynomial
of degree r : $x^3 + x + 1 = P(x)$

2. PARTITION THE OUTPUT STREAM
INTO DISJOINT SUBSTREAMS OF
LENGTH r $T = 2rq$, $r=3$, $q=3$

$$S_1 = 101, S_2 = 011$$

$$S_3 = 110, S_4 = 010$$

$$S_5 = 101, S_6 = 011$$

3. MULTIPLY ANY TWO STRINGS
 S_{2i+1} and S_{2i} AS POLYNOMIALS
MODULO $P(x)$ $(x^3 + x + 1)$

$$\begin{aligned} S_1 \cdot S_2 &= (101)(011) = (1+x^2)(1+x) = \\ &= 1+x^2+x+x^3 = 1+x+x^2+x+1 = x^2 \quad (100) \end{aligned}$$

$$\begin{aligned}
 S_3 \cdot S_4 &= (110)(010) = (x^2+x) \cdot x = \\
 &= x^3 + x^2 = x^2 + x + 1 = (111)
 \end{aligned}$$

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$$S_5 \cdot S_6 = (101)(011) = (x^2+1)(x+1) = 100$$

4. ADD UP RESULTS OF MULTIPLICATIONS TO FORM V -BIT SIGNATURE

$$\begin{aligned}
 S &= S_1 S_2 + S_3 S_4 + S_5 \cdot S_6 = \\
 &= 100 + 111 + 100 = 1111.
 \end{aligned}$$

quadratic
signature.

$$L = s_1 \oplus s_2 \oplus s_3 \oplus s_4 \oplus s_5 \oplus s_6 \rightarrow \text{worst } \underline{\text{LINEAR}} \\ \underline{\text{NOT ROBUST}}$$

$$QS_1 = s_1 \cdot s_2 \oplus s_3 \cdot s_4 \oplus s_5 \cdot s_6$$

$$QS_2 = s_1 \cdot s_3 \oplus s_2 \cdot s_4 \oplus s_5 \cdot s_6$$

} - ROBUST.
 $P_{AL} = 2^{-v}$

QUADRATIC QUADRATIC

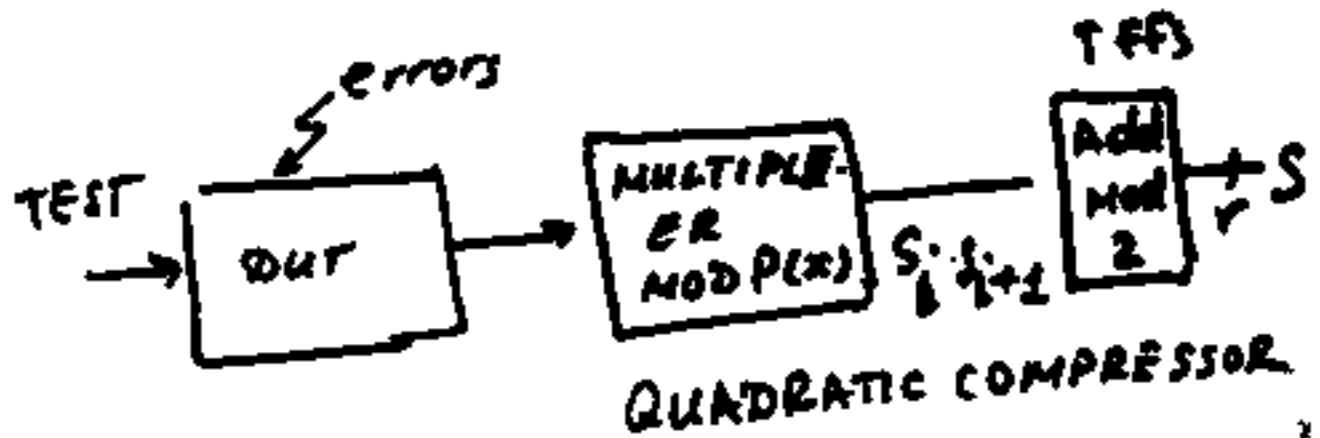
$$CS = s_1 s_2 s_3 \oplus s_4 s_5 s_6$$

.....

} NOT ROBUST
QUBIC

HARDWARE IMPLEMENTATION 23

OF QUADRATIC COMPRESSORS



FINITE FIELD MULTIPLIER MOD P(x)
P(x) - PRIMITIVE

$$q(x) = a(x) \cdot b(x) \pmod{p(x)}$$

$$a(x) = a_{r-1}x^{r-1} + a_{r-2}x^{r-2} + \dots + a_1x + a_0$$

$$b(x) = b_{r-1}x^{r-1} + b_{r-2}x^{r-2} + \dots + b_1x + b_0$$

$$q(x) = q_{r-1}x^{r-1} + q_{r-2}x^{r-2} + \dots + q_1x + q_0$$

$$a_i, b_i, q_i \in \{0, 1\}$$

FINITE FIELD MULTIPLIERS

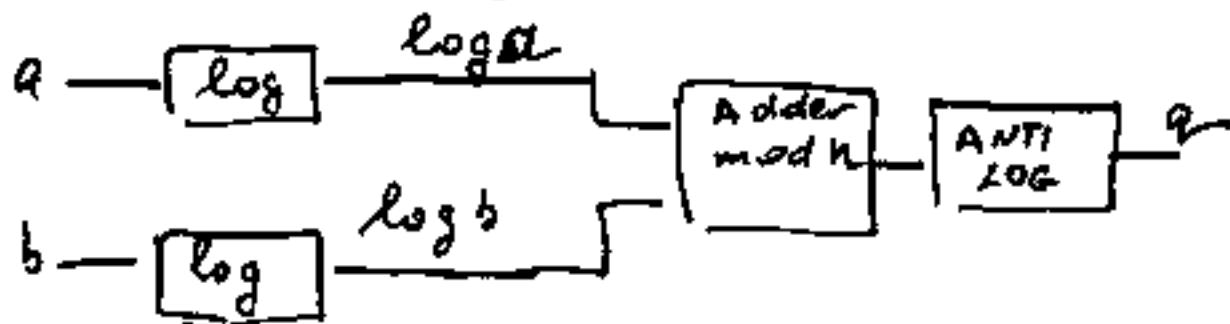
$$\underbrace{(a_0, a_1, \dots, a_{r-1})}_a \underbrace{(b_0, b_1, \dots, b_{r-1})}_b = \underbrace{(q_0, q_1, \dots, q_{r-1})}_q \pmod{p(x)}$$

$$a_i, b_i, q_i \in \{0, 1\}$$

$$\deg p(x) = r$$

$$n = 2^r - 1$$

1. Multiplication by taking log



Example

$$p = x^4 + x + 1$$

$$r = 4 \quad n = 15$$

$$a = 0101 \rightarrow \log a = 8$$

$$b = 1011 \rightarrow \log b = 7$$

$$\rightarrow \log q = 15 \equiv 0 \pmod{15}$$

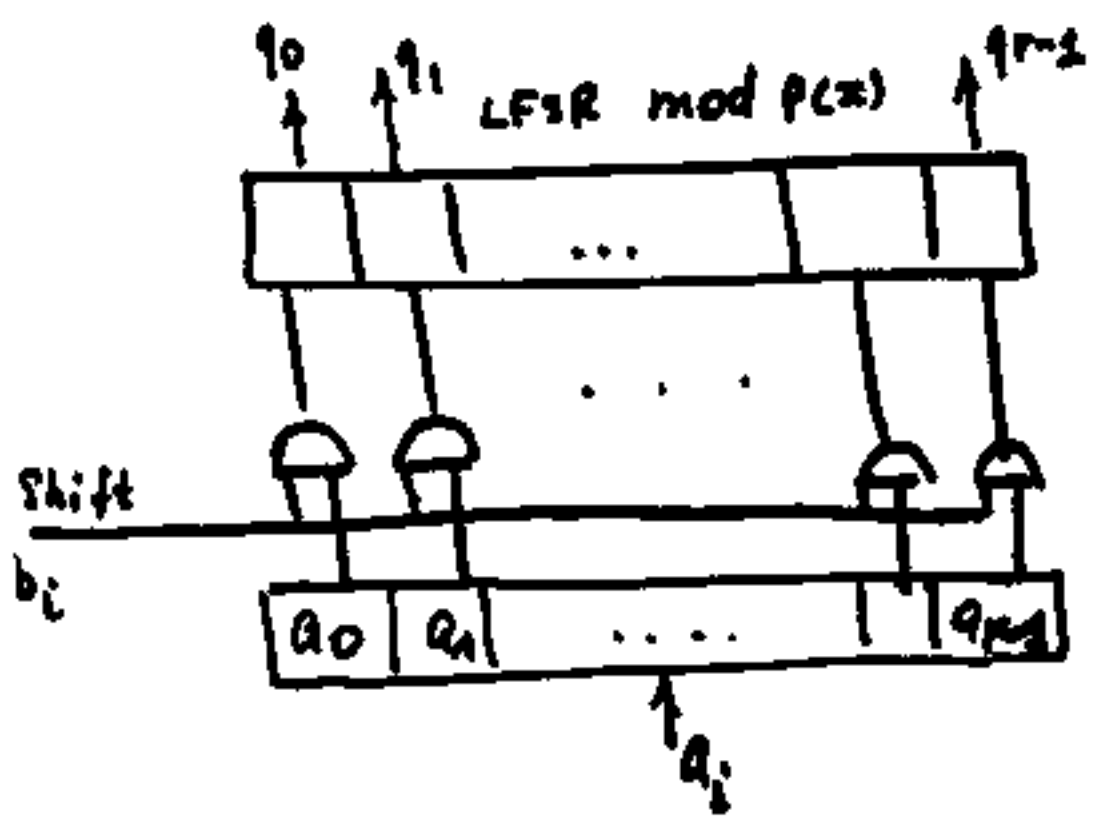
$$\downarrow$$

$$\text{antilog } 0 = 1$$

$$\downarrow$$

$$q = 0001$$

2) SERIAL MULTIPLIER MOD P(x) (24)



r-STEP MULTIPLICATION

3) PARALLEL MULTIPLIER MOD $P(x)$ (25)

EXAMPLE $r=3$ $P(x) = x^3 + x + 1$

$$q(x) = (a_2 x^2 + a_1 x + a_0) (b_2 x^2 + b_1 x + b_0) =$$

$$a_2 b_2 x^4 + (a_1 b_2 + a_2 b_1) x^3 + (a_0 b_2 + a_1 b_1 + a_2 b_0)$$

$$= x^2 + (a_0 b_1 + a_1 b_0) x + a_0 b_0 =$$

$$= c_4 x^4 + c_3 x^3 + c_2 x^2 + c_1 x + c_0$$

Since $x^3 = x + 1$ and $x^4 = x^2 + x \Rightarrow$

$$q(x) = q_2 x^2 + q_1 x + q_0 = (c_4 + c_2) x^2 +$$

$$(c_4 + c_3 + c_1) x + (c_3 + c_0) \pmod{P(x)}$$

Thus

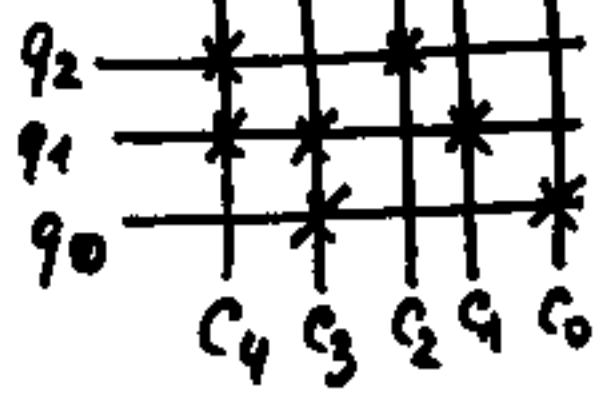
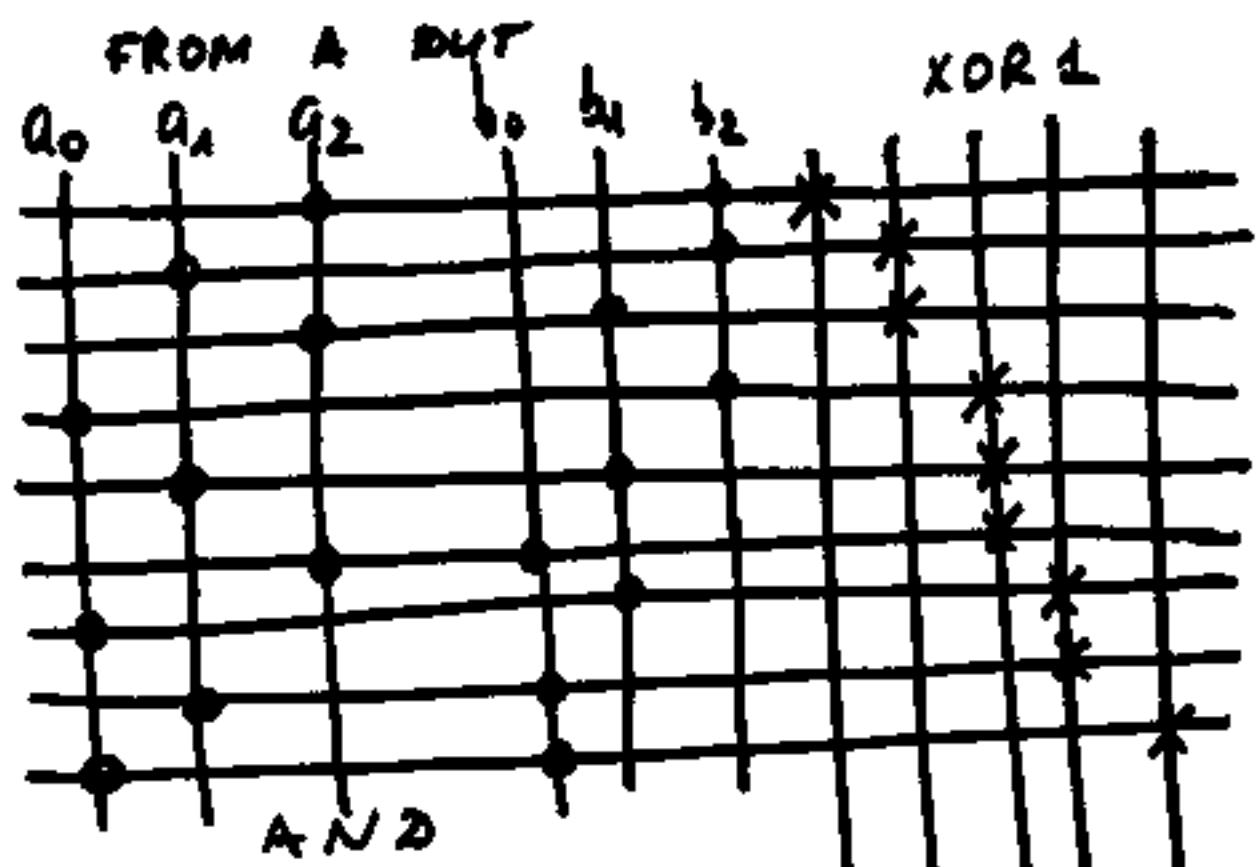
$$q_2 = a_2 b_2 + a_0 b_2 + a_1 b_1 + a_2 b_0$$

$$q_1 = a_2 b_2 + a_1 b_2 + a_2 b_1 + a_0 b_1 + a_1 b_0$$

$$q_0 = a_1 b_2 + a_2 b_1 + a_0 b_0$$

IMPLEMENTATION OF A PARALLEL

MULTIPLIER MOD $P(x) = x^3 + x + 1$



XOR 2
REDUCTION MOD $P(x)$

RESULTS

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QUADRATIC COMPRESSORS ARE ROBUST \Rightarrow ALIASING PROBABILITY IS EQUAL 2^{-v} FOR AN v -bit quadratic signature for any distribution of errors (ASSUMING THAT FAULT-FREE RESPONSES ARE UNIFORMLY distributed)

QUADRATIC COMPRESSORS REQUIRE MORE HARDWARE THAN SIGNATURE ANALYSIS (TWO k -bit LFI'S AND k TWO-INPUT GATES)

N. G. KARPOVSKY, P. NAGVAJARA

"OPTIMAL TIME AND SPACE

COMPRESSION OF TEST RESPONSES

FOR VLSI DEVICES" Proc. INT. TEST

CONF. 1987 pp 523-529

N. G. KARPOVSKY, P. NAGVAJARA

"OPTIMAL COMPRESSORS FOR TEST RESPONSES", IEEE TRANS. ON

COMPUTERS, 1989.

SIMULATION RESULTS

COMPARISON OF QUADRATIC AND

LFSR COMPRESSORS PERFORMANCES.

TWO 181-ALUS

20 TRIALS OF PSEUDORANDOM TEST
SEQUENCES

Circuit	NO TEST PATTERNS	NO FAULTS
8-bit ALU	300	240
12-bit ALU with carry LOOK AHEAD	500	1,866

COMPARISON OF FAULT COVERAGES FOR
LFSRS AND QCS

CIRCUIT	WITHOUT COMPRESSION (% undetected)	4 bit SIGNATURE		8-bit SIGNATURE	
		QC	LFSR	QC	LFSR
8-bit ALU	5.7%	11.4%	11.8%	5.9%	6.1%
12-bit ALU	0.7%	6.6%	7.2%	1.1%	1.2%

DOES NOT
DEPEND
ON DISTRIBUTIONS
OF ERRORS

ANALYSIS OF ALIASING

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PROBABILITIES OF MISRS

Let $\tilde{z}(t)$ - n -bit faulty

response of a DUT
at moment t .

$z(t)$ - n -bit fault-free

response AT MOMENT t .

$$e(t) = (\tilde{z} \oplus z)(t) = \tilde{z}(t) \oplus z(t)$$

error AT MOMENT t .

MULTIPLICITY (WEIGHT) OF $e(t)$:

number of t s such that

$$e(t) \neq \underbrace{(0, \dots, 0)}_n$$

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ASSUMPTION: FOR ANY GIVEN t

all nonzero $e(t)$ are equiprobable.

ERRORS ARE INDEPENDENT IN TIME

Let $P(l)$ is a prob. of ~~deduc-~~ ^{MASKING}tion errors with multiplicity l by an r -bit primitive MISR.

Then

$$P(l) = 2^{-r} (1 + (-1)^l (2^r - 1)^{-l+1})$$

EXAMPLES

$$P(1) = 0 \quad ;$$

$$P(2) = (2^r - 1)^{-1} \quad ;$$

$$P(3) = ((2^r - 1)^2 - (2^r - 1)) (2^r - 1)^{-3} \quad ;$$

$$P(4) = ((2^r - 1)^3 - (2^r - 1)^2 + (2^r - 1)) (2^r - 1)^{-4} \quad \text{etc.}$$

FOR ANY $l > 1$

$$P(l) \approx 2^{-r}$$

for large r .

Let p is a prob that for any given t $\Theta(t) \neq (0, \dots, 0)$ and $T < 2^r$ is a test length. then we have for the aliasing prob.

$$P_{AL} = \sum_{l=1}^T \binom{T}{l} \left(\frac{p}{2^n - 1} \right)^l (1-p)^{T-l}.$$

$$2^{-r} \left((2^r - 1)^l + (-1)^l (2^r - 1) \right)$$

FOR INDEPENDENT ERRORS OF EQUAL PROBABILITY (TIME INDEPENDENT SPACE SYMMETRICAL ERRORS)

FOR LARGE n

$$P_{AL} \approx 2^{-n}$$

D.K. Pradhan, S.K. GUPTA AND

M.G. KARPOVSKY

"ALIASING PROBABILITY AND A
NEW COMPRESSION TECHNIQUE"

IEEE TRANS. ON COMPUTERS, 1990.

Conclusions

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SYNDROME TESTING, TRANSITION COUNTING AND SIGNATURE ANALYSIS ARE TIME COMPRESSION TECHNIQUES.

FOR TRANSITION COUNTING AND SIGNATURE ANALYSIS ALIASING PROBABILITY DEPENDS ON AN ORDER OF TEST PATTERNS

PARALLEL SIGNATURE ANALYZERS ARE THE MOST POPULAR TIME COMPRESSORS.

SYNDROME TESTING, TRANSITION COUNTING AND SIGNATURE ANALYSIS ARE NOT ROBUST (ALIASING PROB. DEPENDS ON DISTRIBUTIONS OF ERRORS)